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IN THE DRAWING

A REPLACEMENT SHEET is submitted herewith identifying Fig. 1 as prior art.

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REMARKS

The Office Action of 12/18/1006 has been carefully considered. Reconsideration in view of the foregoing amendments and the present remarks is respectfully requested.

Claims 1-3 were rejected as being anticipated by Raynaud. Claim 4 was rejected as being unpatentable over Raynaud in view of Kovacs. The claims have been amended to more clearly distinguish over the cited references. Reconsideration is respectfully requested.

In particular, claim 1 has been amended to recite that the conversion circuitry generates an output signal with a level proportional to the square of the effective value of the attenuated signal, a feature previously recited in claim 2. In relation to this feature, the Office Action stated: "[R]aynaud et al. clearly disclose...said conversion means (figure 1, diode detector 15) comprise processing means for generating said output signal with a level proportional to the square of the effective value of said attenuated signal (figure 1, column 4 lines 5-10, diode detector 15 converts attenuated signal into a suitable voltage value Vd as a input of operational amplifier 16 for comparing purpose)."

A diode detector such as diode detector 15, however, does not perform the recited function, namely generating an output signal with a level proportional to the square of the effective value of the attenuated signal. Rather, the diode detector functions as a peak detector like that of Figure 1 of the present specification. As described therein, such a detector has been discovered to be disadvantageous as applied to a tuner in terms of cross-modulation, intermodulation and pulling effects.

Withdrawal of the rejections and allowance of claims 1-6 is respectfully requested.

Respectfully submitted,

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Dated: 05/16/2007